The **program status word** (**PSW**) is a register that performs the function of a [status register](https://en.wikipedia.org/wiki/Status_register) and [program counter](https://en.wikipedia.org/wiki/Program_counter), and sometimes more.

The 64-bit PSW describes (among other things)

* [Interrupt masks](https://en.wikipedia.org/wiki/Interrupt_mask)
* Privilege states
* Condition code
* Instruction address